



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2829
Examiner: VELEZ, ROBERTO

In Re PATENT APPLICATION Of:

Applicants : Toshio TERAISHI

Appln. No. : 10 /617,817

Filed : July 14, 2003

For : ANALOG VOLTAGE OUTPUT
DRIVER LSI CHIP HAVING TEST
CIRCUIT

Attorney Ref.: 03DCOAI030

Commissioner for Patents
P.O. Box 1450
Alexandria, Va 22313-1450

AMENDMENT

February 13, 2008

Sir:

In response to the Examiner's Office Action mailed August 24, 2007, which has been extended to expire on November 24, 2004 by a Petition for a three-month Extension of Time filed herewith, this Amendment is submitted with the applicant's Request for Continued Examination pursuant to 37 C.F.R. §1.114(a). Please amend the above-identified application as follows: